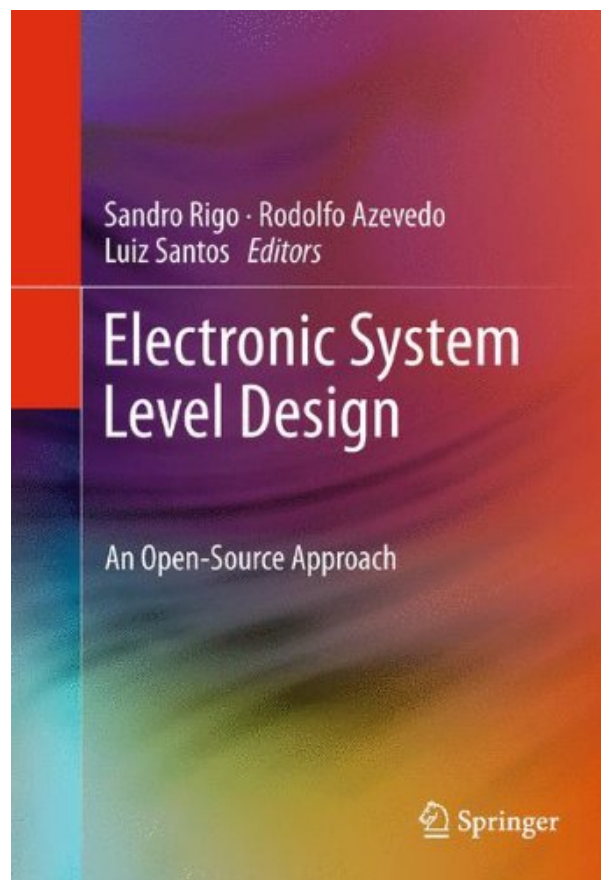
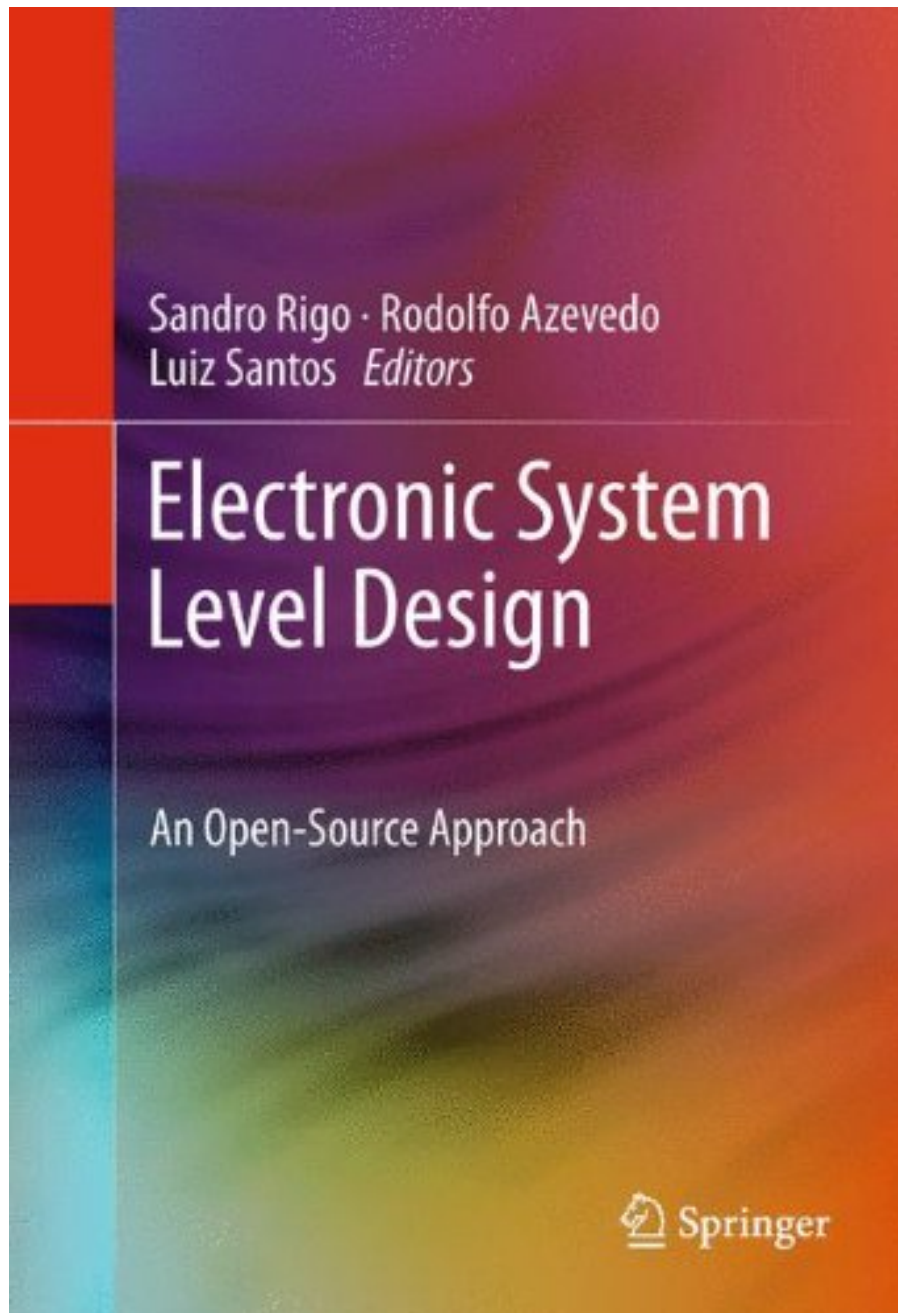


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From the Back Cover

This book intends to provide grounds for further research on electronic system level design (ESL), by means of open-source artifacts and tools, thereby stimulating the unconstrained deployment of new concepts, tools, and methodologies. It devises ESL design from the pragmatic perspective of a SystemC-based representation, by showing how to build and how to use ESL languages, models and tools.

This work is based on the successful experience acquired with the conception of the architecture description language (ADL) ArchC, the development of its underlying tool suite, and the building of its platform modeling infrastructure. With more than 10,000 accesses per year since 2004, the dissemination of ArchC models has reached not only students in need of proper infrastructure to develop their research projects, but also commercial designers in need of processor models to build virtual platforms using SystemC.

The need to anticipate the development of hardware-dependent software and to build virtual prototypes gave rise to transaction level modeling (TLM). Since SystemC provided the elements and the adequate abstraction level for supporting TLM, their relation has grown so strong that OSCI created a TLM Working Group whose effort resulted in the TLM 2.0 standard, which is also covered in this book.

- Employs open-source infrastructure
- Includes TLM 2.0
- Addresses power modeling
- Includes step-by-step examples

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- Sales Rank: #7080024 in Books
- Brand: Brand: Springer
- Published on: 2011-05-12
- Original language: English
- Number of items: 1
- Dimensions: 9.21" h x .44" w x 6.14" l, .85 pounds
- Binding: Hardcover
- 146 pages

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